

| L Number | Hits | Search Text | DB | Time stamp |
|----------|------|--|--------------------------|------------------|
| 1 | 0 | 712/205.ccls | USPAT; EPO; JPO | 2003/01/23 09:58 |
| 2 | 188 | 712/205.ccls. | USPAT; EPO; JPO | 2003/01/23 09:58 |
| 3 | 143 | 712/206.ccls. | USPAT; EPO; JPO | 2003/01/23 09:58 |
| 4 | 286 | 712/207.ccls. | USPAT; EPO; JPO | 2003/01/23 09:58 |
| 5 | 12 | (queue buffer) with (multithread\$4 multi-thread\$4) with (instruction section portion part) | USPAT; EPO; JPO | 2003/01/23 10:29 |
| | 78 | (microinstruction micro-instruction) with queue | USPAT; EPO; JPO; IBM TDB | 2003/01/16 14:38 |
| | 2 | (microinstruction micro-instruction) with queue with valid | USPAT; EPO; JPO; IBM TDB | 2003/01/16 14:38 |
| | 45 | ((microinstruction micro-instruction) with queue) and valid | USPAT; EPO; JPO; IBM TDB | 2003/01/22 09:51 |
| | 2 | ("4682284" "5954815").pn. | USPAT; EPO; JPO; IBM TDB | 2003/01/21 14:00 |
| | 0 | 08445563.ap. | USPAT; EPO; JPO; IBM TDB | 2003/01/21 14:01 |
| | 0 | "08445563" | USPAT; EPO; JPO; IBM TDB | 2003/01/21 14:01 |
| | 4 | 445563.ap. | USPAT; EPO; JPO; IBM TDB | 2003/01/21 14:50 |
| | 1701 | bypass with (queue buffer) | USPAT; EPO; JPO; IBM TDB | 2003/01/21 14:50 |
| | 0 | bypass with (queue buffer) with (microinstruction micro-instruction) | USPAT; EPO; JPO; IBM TDB | 2003/01/21 15:02 |
| | 5 | bypass with (queue buffer) same (microinstruction micro-instruction) | USPAT; EPO; JPO; IBM TDB | 2003/01/21 15:02 |
| | 22 | (queue buffer) with multithreaded | USPAT; EPO; JPO; IBM TDB | 2003/01/22 16:00 |
| | 211 | ((microinstruction micro-instruction) with buffer) and valid | USPAT; EPO; JPO; IBM TDB | 2003/01/22 09:54 |
| | 26 | ((microinstruction micro-instruction) with buffer) same cache) and valid | USPAT; EPO; JPO; IBM TDB | 2003/01/22 10:02 |
| | 118 | microinstruction with cache | USPAT | 2003/01/22 15:34 |
| | 45 | microinstruction near4 cache | USPAT | 2003/01/22 15:51 |
| | 41 | macroinstruction near4 cache | USPAT | 2003/01/22 15:52 |
| | 5 | (microinstruction near4 cache) and (macroinstruction near4 cache) | USPAT | 2003/01/22 15:52 |
| | 0 | (queue buffer) with multithreaded with (portion section) | USPAT; EPO; JPO | 2003/01/22 16:01 |
| | 0 | (queue buffer) with multithreaded with (portion section part) | USPAT; EPO; JPO | 2003/01/22 16:02 |
| | 2 | (queue buffer) with multithreaded with (portion section partition\$4) | USPAT; EPO; JPO | 2003/01/22 16:04 |
| | 3 | (queue buffer) with multithreaded with instruction | USPAT; EPO; JPO | 2003/01/23 10:28 |